

- ☒ BRS:
- ☒ Pending
  - ☒ Active
    - ☒ L1: (2677) adding with most with (one or "1")
    - ☒ L2: (691861) round\$3
    - ☒ L3: (33) 1 same 2
    - ☒ L4: (25) 1 with 2
    - ☒ L5: (6) 4648058.URPN.
    - ☒ L6: (8) 3 not 4
    - ☒ L7: (697) sign near10 2
    - ☒ L8: (3) 1 same 7
    - ☒ L9: (3506527) select\$3
    - ☒ L10: (308792) increment\$3
    - ☒ L11: (177) 2 with 9 with 10
    - ☒ L12: (22404) 708/.cccls.
    - ☒ L13: (55) 11 and 12
    - ☒ L14: (1) ("5091676").PN.
    - ☒ L25: (2) ("5233605").PN.
  - ☒ Failed
  - ☒ Saved
    - ☒ (362) 708/550,551,497,498.cccls.
    - ☒ (192) non adj bias
    - ☒ (0) 708/550,551,497,498.cccls. and (non adj b
    - ☒ (436259) bias
    - ☒ (61) 708/550,551,497,498.cccls. and bias
    - ☒ (1269) round\$3 with bias
    - ☒ (16) 708/550,551,497,498.cccls. and (round\$3
  - ☒ Favorites
  - ☒ Tagged (5)
  - ☒ UDC

Search List Browse Queue Clear

DBs USPAT: US-PGPUB: EPO: JPO: DERWENT: IBM: TI: Biurels

Default operator: OR Highlight all hit terms initially

BRD term SAR term Issue Text HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	Inventor	S	C	P	3	U
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 4648058 A	19870303	7	Look-ahead rounding circuit	708/551	708/672		Masumoto, Rodney T	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6055555 A	20000425	9	Interface for performing parallel arithmetic and round operations	708/551			Boswell, Brent et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 5200916 A	19930406	21	Mantissa processing circuit of floating point arithmetic apparatus	708/505			Yoshida, Tadahiro	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
4	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6615228 B1	20030902	13	Selection based rounding system and method for floating	708/497			Colon-Bonet, Glenn T et al	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
5	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 4977535 A	19901211	7	Method of computation of normalized numbers	708/497	708/205		Birger, Ari	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>